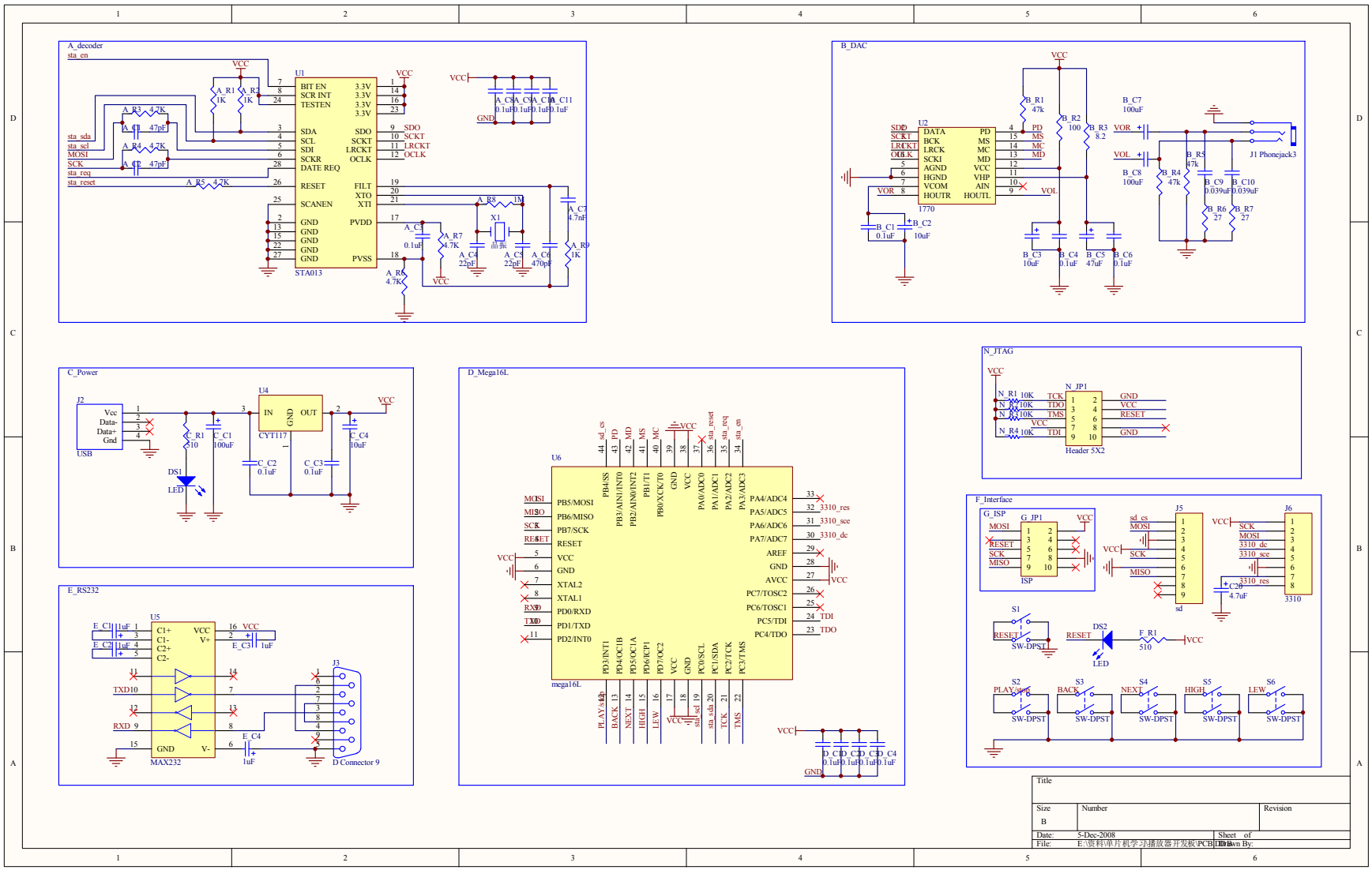
A_decoder
sta_en
VCC
U1
BIT EN
SCR INT
TESTEN
SDA
SCL
SDI
SCKR
DATE REQ
RESET
SCANEN
GND
PVDD
PVSS
3.3V
SDO
SCKT
LRCKT
OCLK
FILT
XTO
XTI
STA013
A_R1 1K
A_R2 1K
A_R3 4.7K
A_R4 4.7K
A_R5 4.7K
A_R6 4.7K
A_R7 4.7K
A_R8 1M
A_R9 1K
A_C1 47pF
A_C2 47pF
A_C3 0.1uF
A_C4 22pF
A_C5 22pF
A_C6 470pF
A_C7 4.7nF
A_C8 0.1uF
A_C9 0.1uF
A_C10 0.1uF
A_C11 0.1uF
X1
sta_sda
sta_scl
MOSI
SCK
sta_req
sta_reset
B_DAC
U2
DATA
BCK
LRCK
SCKI
AGND
HGND
VCOM
HOUTR
PD
MS
MC
MD
VCC
VHP
AIN
HOUTL
1770
B_R1 47k
B_R2 100
B_R3 8.2
B_R4 47k
B_R5 47k
B_R6 27
B_R7 27
B_C1 0.1uF
B_C2 10uF
B_C3 10uF
B_C4 0.1uF
B_C5 47uF
B_C6 0.1uF
B_C7 100uF
B_C8 100uF
B_C9 0.039uF
B_C10 0.039uF
VOR
VOL
J1 Phonejack3
C_Power
J2
USB
Vcc
Data-
Data+
Gnd
U4
CYT117
IN
GND
OUT
C_R1 10
C_C1 100uF
C_C2 0.1uF
C_C3 0.1uF
C_C4 10uF
DS1
LED
D_Mega16L
U6
mega16L
PB5/MOSI
PB6/MISO
PB7/SCK
RESET
VCC
GND
XTAL2
XTAL1
PD0/RXD
PD1/TXD
PD2/INT0
PB4/SS
PB3/AIN1/INT0
PB2/AIN0/INT2
PB1/T1
PB0/XCK/T0
PA0/ADC0
PA1/ADC1
PA2/ADC2
PA3/ADC3
PA4/ADC4
PA5/ADC5
PA6/ADC6
PA7/ADC7
AREF
AVCC
PC7/TOSC2
PC6/TOSC1
PC5/TDI
PC4/TDO
PD3/INT1
PD4/OC1B
PD5/OC1A
PD6/ICP1
PD7/OC2
PC0/SCL
PC1/SDA
PC2/TCK
PC3/TMS
3310_res
3310_sce
3310_dc
TDI
TDO
TMS
TCK
N_JTAG
N_JP1
Header 5X2
N_R1 10K
N_R2 10K
N_R3 10K
N_R4 10K
F_Interface
G_ISP
G_JP1
ISP
J5
sd
J6
3310
C26 4.7uF
S1
RESET
SW-DPST
DS2
F_R1 510
S2
PLAY/PAUSE
S3
BACK
S4
NEXT
S5
HIGH
S6
LEW
E_Interface
U5
MAX232
E_C1 1uF
E_C2 1uF
E_C3 1uF
E_C4 1uF
TXD
RXD
J3
D Connector 9
Title
Size
B
Number
Revision
Date: 5-Dec-2008
Sheet of
File: E:\资料\单片机学习\播放器开发板\PCB\DDraw By: