

P0	IC1A	P1
1 P0.0	P0.0/RD1/TXD3/SDA1	P1.0/ENET_TXD0
2 P0.1	P0.1/TD1/RXD3/SCL1	P1.1/ENET_TXD1
3 P0.2	P0.2/TXD0/AD0.7	P1.4/ENET_TX_EN
4 P0.3	P0.3/RXD0/AD0.6	P1.8/ENET_CRS
5 P0.4	P0.4/I2SRX_CLK/RD2/CAP2.0	P1.9/ENET_RXD0
6 P0.5	P0.5/I2SRX_WS/TD2/CAP2.1	P1.10/ENET_RXD1
7 P0.6	P0.6/I2SRX_SDA/SSEL1/MAT2.0	P1.14/ENET_RX_ER
8 P0.7	P0.7/I2STX_CLK/SCK1/MAT2.1	P1.15/ENET_REF_CLK
9 P0.8		P1.16/ENET_MDC
10 P0.9	P0.8/I2STX_WS/MISO1/MAT2.2	P1.17/ENET_MDIO
11 P0.10	P0.9/I2STX_SDA/MOSI1/MAT2.3	
12 P0.11	P0.10/TXD2/SDA2/MAT3.0	P1.18/USB_UP_LED/PWM1.1/CAP1.0
13 P0.15	P0.11/RXD2/SCL2/MAT3.1	P1.19/MC0A/nUSB_PPWR/CAP1.1
14 P0.16	P0.15/TXD1/SCK0/SCK	P1.20/MCFB0/PWM1.2/SCK0
15 P0.17		P1.21/MCABORT/PWM1.3/SSEL0
16 P0.18	P0.16/RXD1/SSEL0/SSEL	P1.22/MC0B/USB_PWRD/MAT1.0
17 P0.19	P0.17/CTS1/MISO0/MISO	P1.23/MCFB1/PWM1.4/MISO0
18 P0.20	P0.18/DCD1/MOSI0/MOSI	P1.24/MCFB2/PWM1.5/MOSI0
19 P0.21	P0.19/DSR1/SDA1	P1.25/MC1A/MAT1.1
20 P0.22	P0.20/DTR1/SCL1	P1.26/MC1B/PWM1.6/CAP0.0
21 P0.23	P0.21/RI1/RD1	P1.27/CLKOUT/nUSB_OVRCR/CAP0.1
22 P0.24	P0.22/RTS1/TD1	P1.28/MC2A1.0/MAT0.0
23 P0.25	P0.23/AD0.0/I2SRX_CLK/CAP3.0	P1.29/MC2B/PCAP1.1/MAT0.1
24 P0.26		P1.30/VBUS/AD0.4
25 P0.27	P0.24/AD0.1/I2SRX_WS/CAP3.1	P1.31/SCK1/AD0.5
26 P0.28	P0.25/AD0.2/I2SRX_SDA/TXD3	
27 P0.29	P0.26/AD0.3/AOUT/RXD3	
28 P0.30	P0.27/SDA0/USB_SDA	P2.0/PWM1.1/TXD1
	P0.28/SCL0/USB_SCL	P2.1/PWM1.2/RXD1
	P0.29/USB_D+	P2.2/PWM1.3/CTS1/TRACEDATA3
	P0.30/USB_D-	P2.3/PWM1.4/DCD1/TRACEDATA2
		P2.4/PWM1.5/DSR1/TRACEDATA1
		P2.5/PWM1.6/DTR1/TRACEDATA0
		P2.6/PCAP1.0/RI1/TRACECLK
		P2.7/RD2/RTS1
		P2.8/TD2/TXD2
		P2.9/USB_CONNECT/RXD2
		P2.10/nEINT0/NMI
		P2.11/nEINT1/I2STX_CLK
		P2.12/nEINT2/I2STX_WS
		P2.13/nEINT3/I2STX_SDA

P3 P4

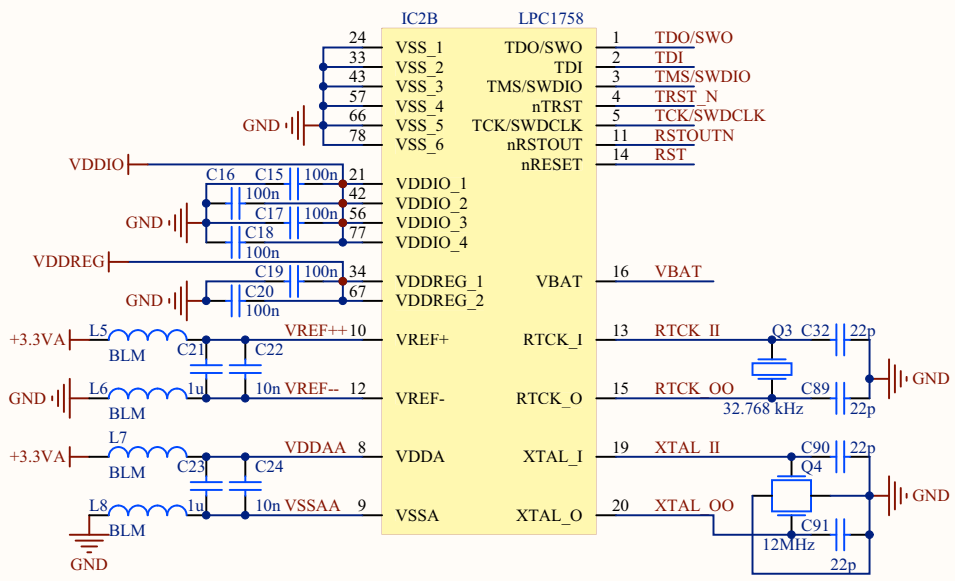
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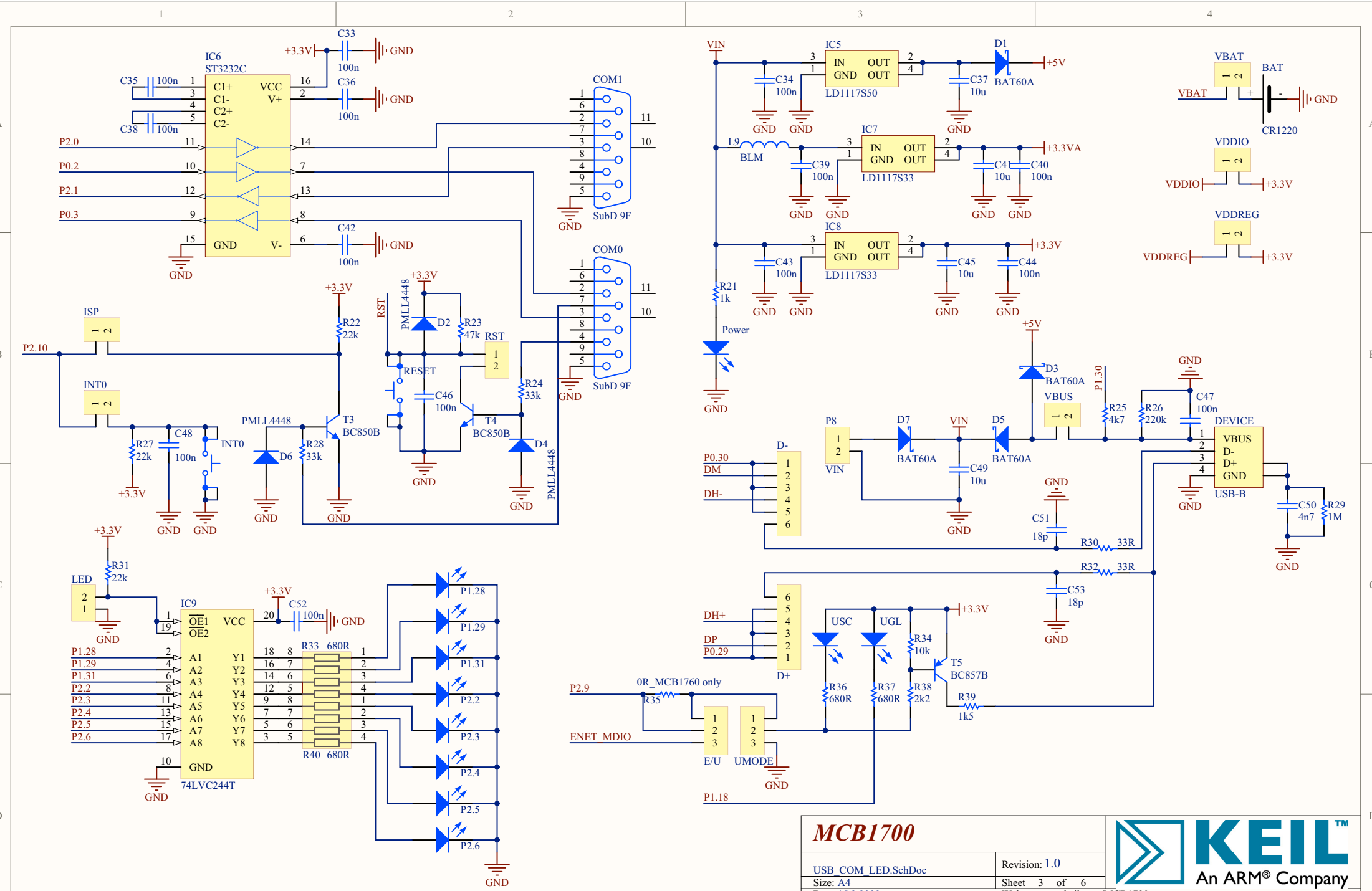
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P0.1	38	P0.1/TD1/RXD3/SCL1	P1.1/ENET_TXD1	75	P1.1
P0.2	79	P0.2/TXD0/AD0.7	P1.4/ENET_TX_EN	74	P1.4
P0.3	80	P0.3/RXD0/AD0.6	P1.8/ENET_CRSS	73	P1.8
P0.6	64	P0.6/I2SRX_SDA/SSEL1/MAT2.0	P1.9/ENET_RXD0	72	P1.9
P0.7	63	P0.7/I2STX_CLK/SCK1/MAT2.1	P1.10/ENET_RXD1	71	P1.10
P0.8	62	P0.8/I2STX_WS/MISO1/MAT2.2	P1.14/ENET_RX_ER	70	P1.14
P0.9	61	P0.9/I2STX_SDA/MOSI1/MAT2.3	P1.15/ENET_REF_CLK	69	P1.15
P0.10	39	P0.10/TXD2/SDA2/MAT3.0			
P0.11	40	P0.11/RXD2/SCL2/MAT3.1	P1.18/USB_UP_LED/PWM1.1/CAP1.0	25	P1.18
P0.15	47	P0.15/TXD1/SCK0/SCK	P1.19/MC0A/nUSB_PPWR/CAP1.1	26	P1.19
P0.16	48	P0.16/RXD1/SSEL0/SSEL	P1.20/MCFB0/PWM1.2/SCK0	27	P1.20
P0.17	46	P0.17/CTS1/MISO0/MISO	P1.22/MC0B/USB_PWRD/MAT1.0	28	P1.22
P0.18	45	P0.18/DCD1/MOSI0/MOSI	P1.23/MCFB1/PWM1.4/MISO0	29	P1.23
P0.22	44	P0.22/RTS1/TD1	P1.24/MCFB2/PWM1.5/MOSI0	30	P1.24
P0.25	7	P0.25/AD0.2/I2SRX_SDA/TXD3	P1.25/MC1A/CLKOUT/MAT1.1	31	P1.25
P0.26	6	P0.26/AD0.3/AOUT/RXD3	P1.26/MC1B/PWM1.6/CAP0.0	32	P1.26
P0.29	22	P0.29/USB_D+	P1.28/MC2A/PCAP1.0/MAT0.0	35	P1.28
P0.30	23	P0.30/USB_D-	P1.29/MC2B/PCAP1.1/MAT0.1	36	P1.29
			P1.30/VBUS/AD0.4	18	P1.30
			P1.31/SCK1/AD0.5	17	P1.31
			P2.0/PWM1.1/TXD1	60	P2.0
			P2.1/PWM1.2/RXD1	59	P2.1
			P2.2/PWM1.3/CTS1/TRACEDATA3	58	P2.2
			P2.3/PWM1.4/DCD1/TRACEDATA2	55	P2.3
			P2.4/PWM1.5/DSR1/TRACEDATA1	54	P2.4
			P2.5/PWM1.6/DTR1/TRACEDATA0	53	P2.5
			P2.6/PCAP1.0/RI1/TRACECLK	52	P2.6
P4.28	65	P4.28/RX_MCLK/MAT2.0/TXD3	P2.7/RD2/RTS1	51	P2.7
P4.29	68	P4.29/TX_MCLK/MAT2.1/RXD3	P2.8/TD2/TXD2/ENET_MDC	50	P2.8
			P2.9/USB_CONNECT/RXD2/ENET_MDIO	49	P2.9
			P2.10/nEINT0/NMI	41	P2.10

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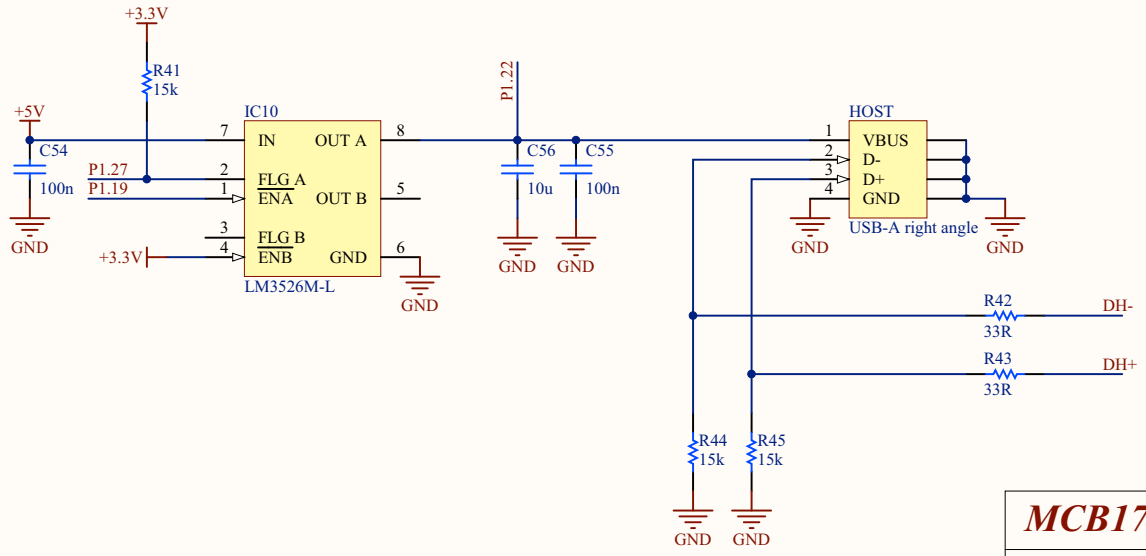
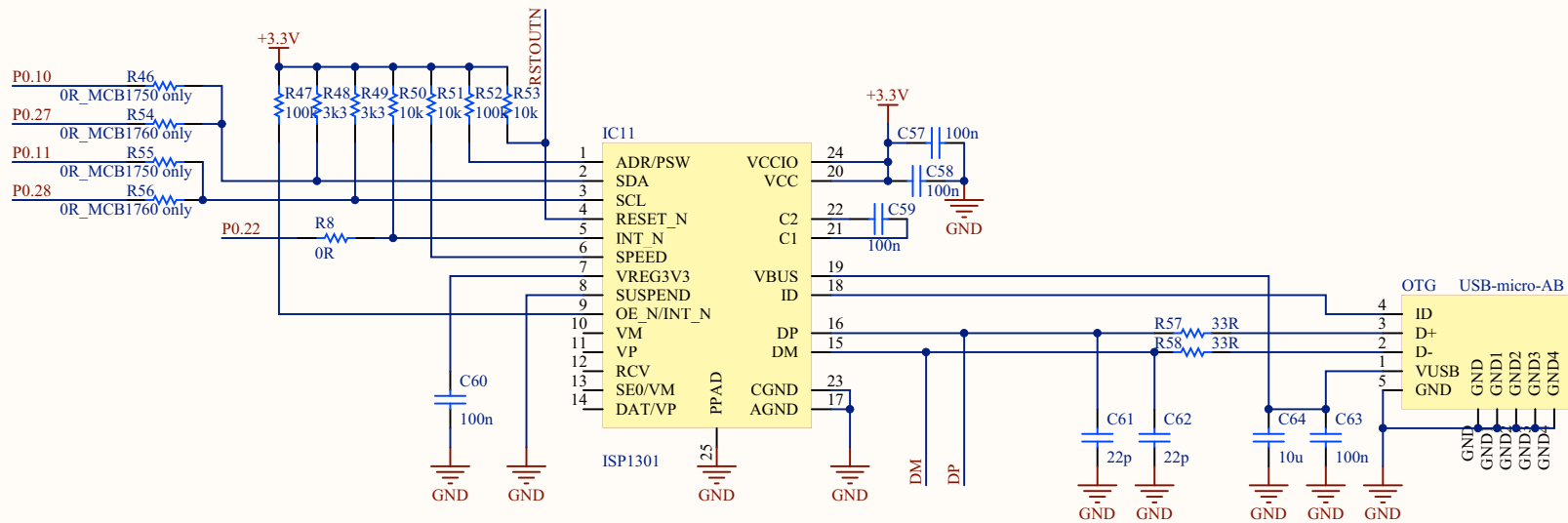


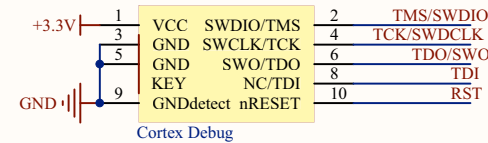
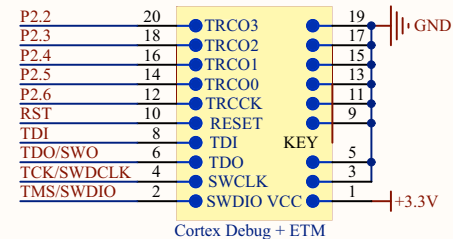
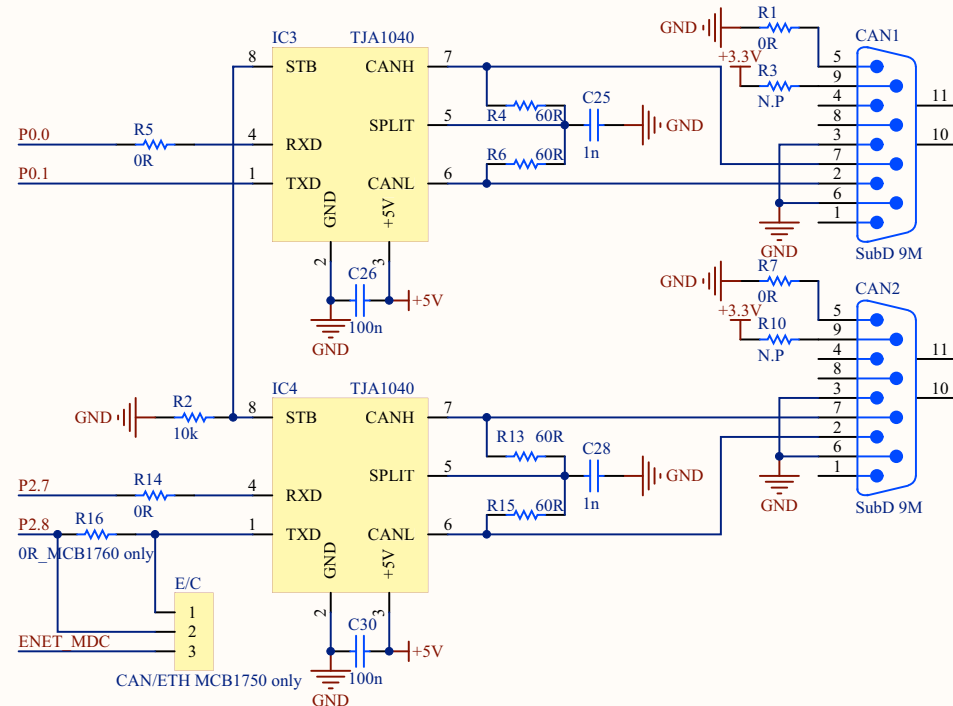
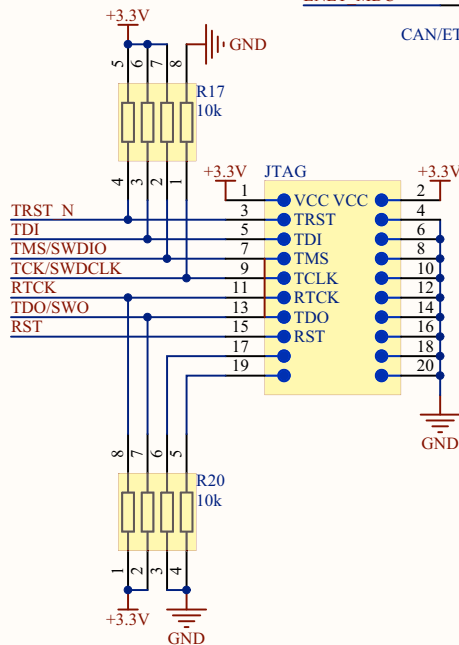
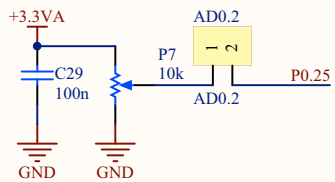
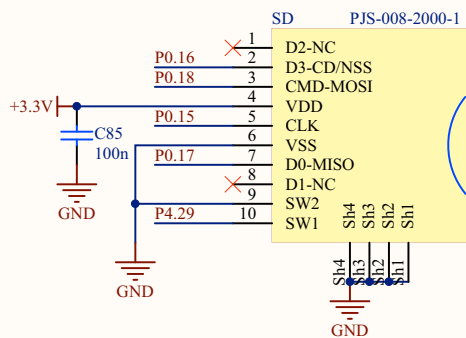
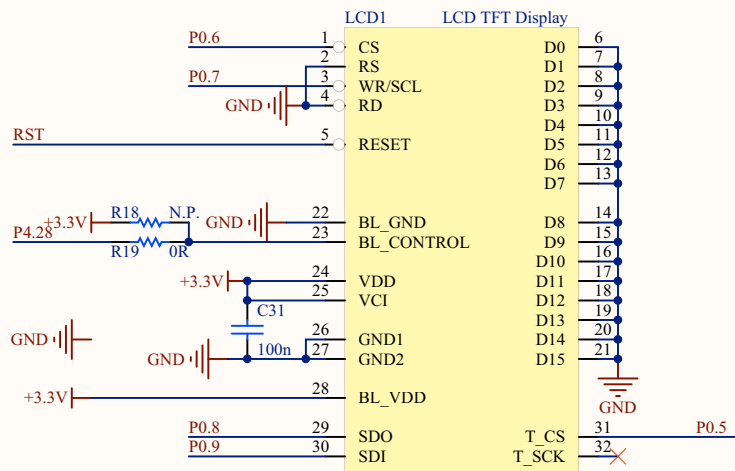


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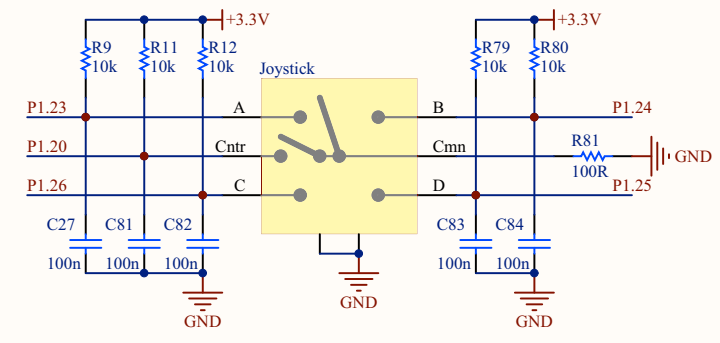
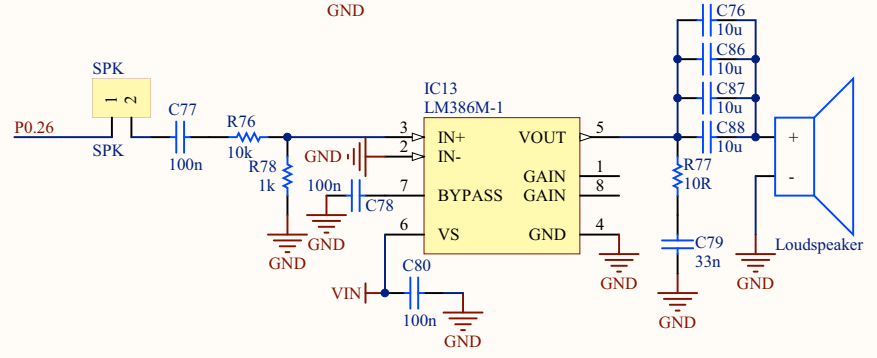
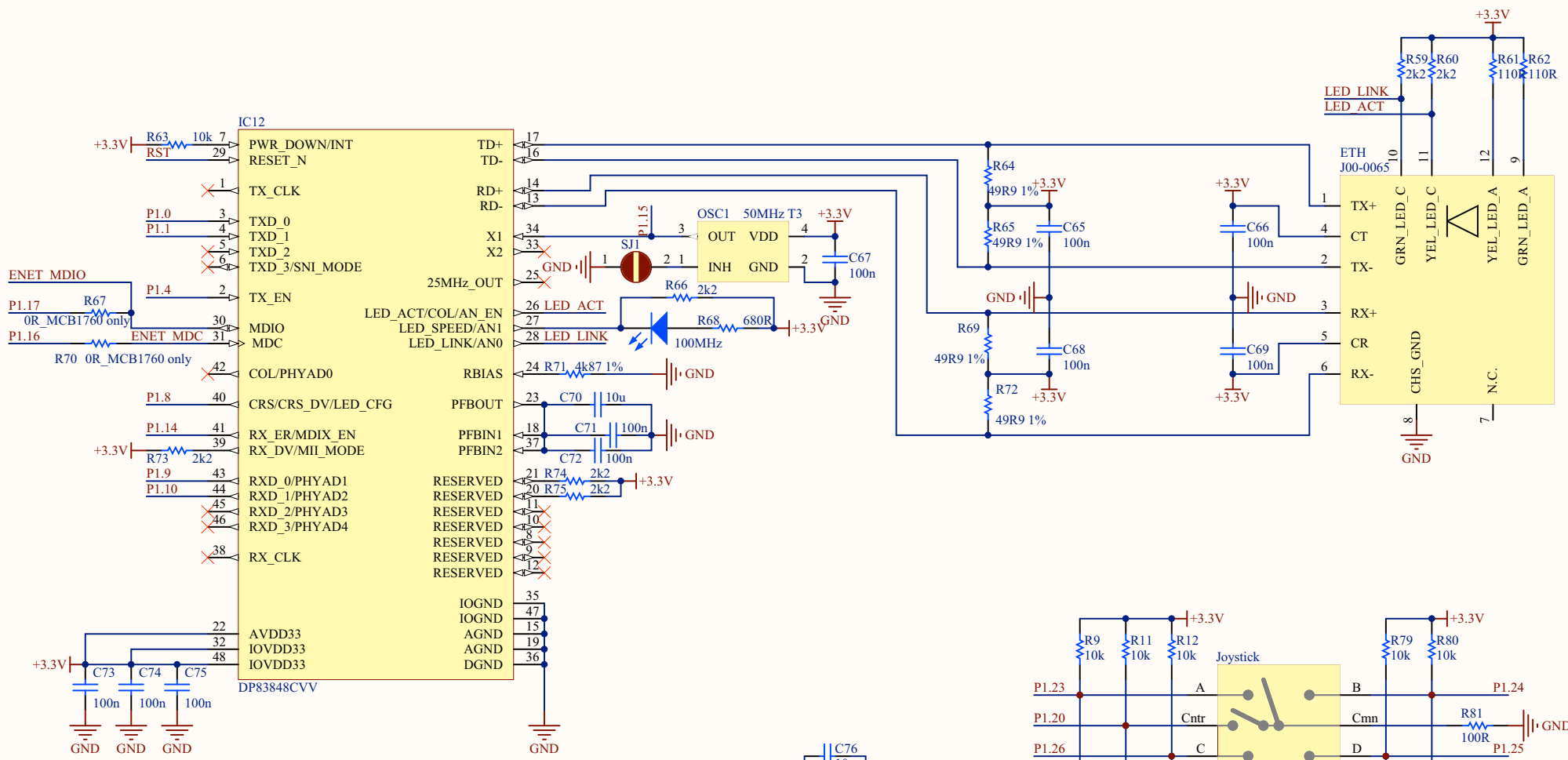
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