Enough to go to the Moon (and back) JavaCard™ Technology

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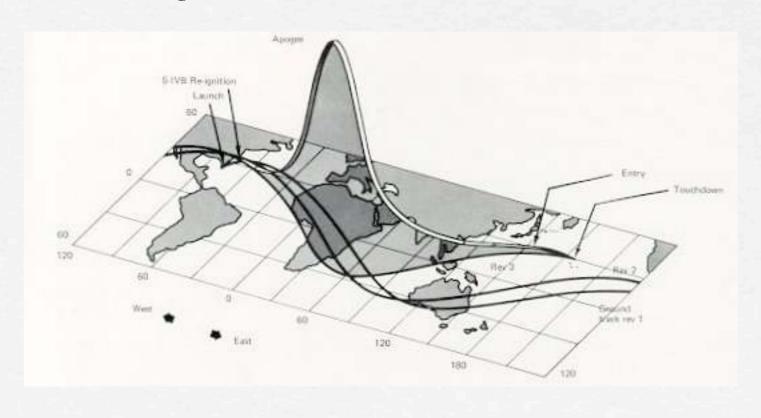
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Flight Plan

- Smartcard Technology
- Javacard Technology
- 1BMJCOP
- Demo
- Outlook





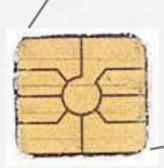
SmartCard Hardware

CPU 8-32 Bít Cryptounits

1/0 - 800 Kbps

RAM 1-8 KB EEPROM 4-72 KB

ROM - 200 KB





Tamper Resistance

- Shielding against microprobing
- Sensors (glitch, light, temperature, ...)
- Randomízed chíp layout
- Randomízed clock
- unified power consumption
- Memory encryption





Apollo 11 GC

- 2-Mhz CPU
- 16 Bit Word Length
- 2 KB RAM
- 36 KB ROM
- http://www.apollosaturn.com/gnc.htm http://www.digitalmist.com/plethorama/apollogc.htm





Smart "cards" in the Wild











Proprietary vs...

- File-System-Based
- Programming in assembly and/or c
- Vendor Lock-In
 - os gapplications from single vendor
 - Moving to another vendor becomes cost/time intensive





...Open

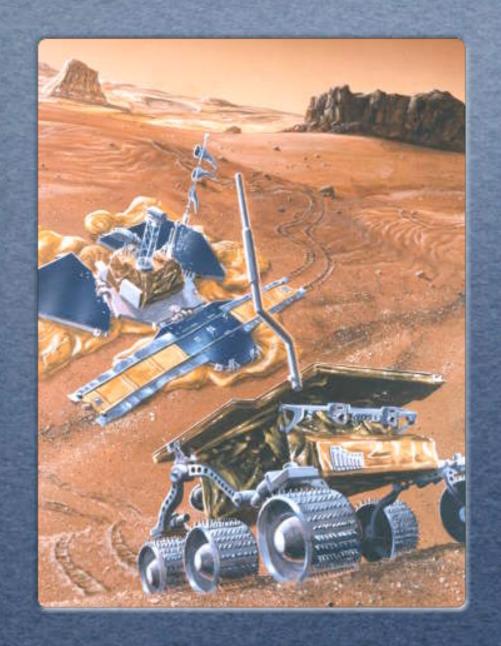
- Hardware abstraction (VM)
- High-level programming language
 - Dbject-oriented
- open Standards
- PC Model: Hardware/OS/Applications





JavaCard* (& Global Platform)

"The Master Plan"

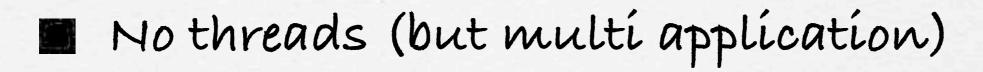




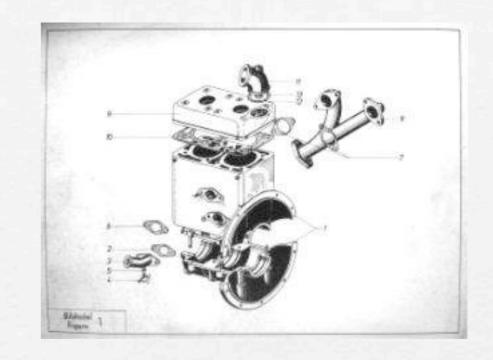
*JavaCard is a trademark of Sun Microsystems Inc. in the United States and other Countries.

JavaCard VM

- Extended memory model
- Transactions



- Java Programming Language
 - Subset: No float, optional int





JavaCard Security

- Sandboxing
- Firewall (applet isolation/sharing)
- Byte-code verification
 - Off-card (signed applets)
 - on-card verification on the horizon



Global Platform

- Multi-Application Token-Security Framework
 - Applet loading, installation, removal
 - Secure Messaging
 - Security Domains



JC/GP Standard APIs

- Javacard
 - Applet Base, Communication, ...
 - Crypto: Ciphers, Digests, Key Mgmt
- Global Platform
 - Secure Messaging





JC/GP Architecture

ROM OF EEPROM

PKCS#15

visacash

. . .

Apollo 11

Operating System (in ROM)

Javacard

Global Platform

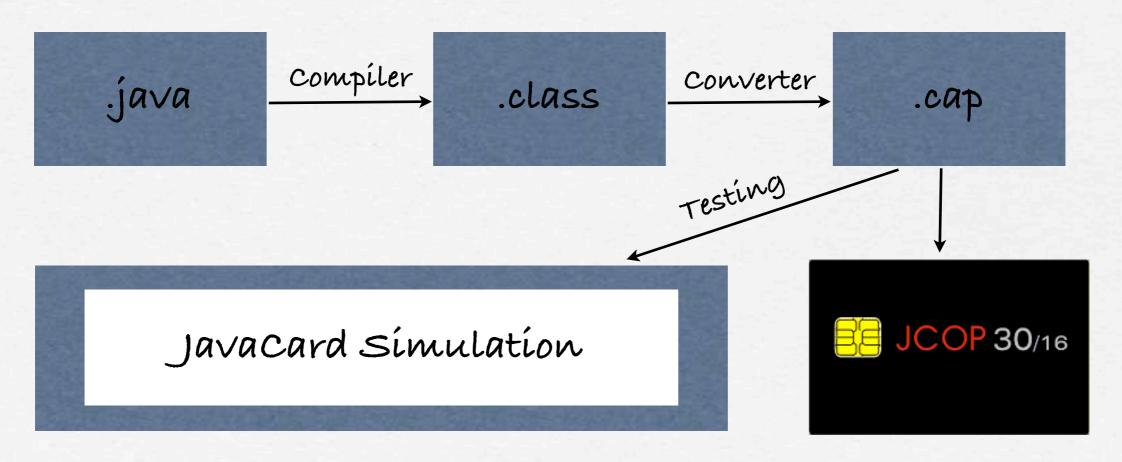
Javacard VM I low-level drivers for Comm. & Crypto I

Smartcard Hardware

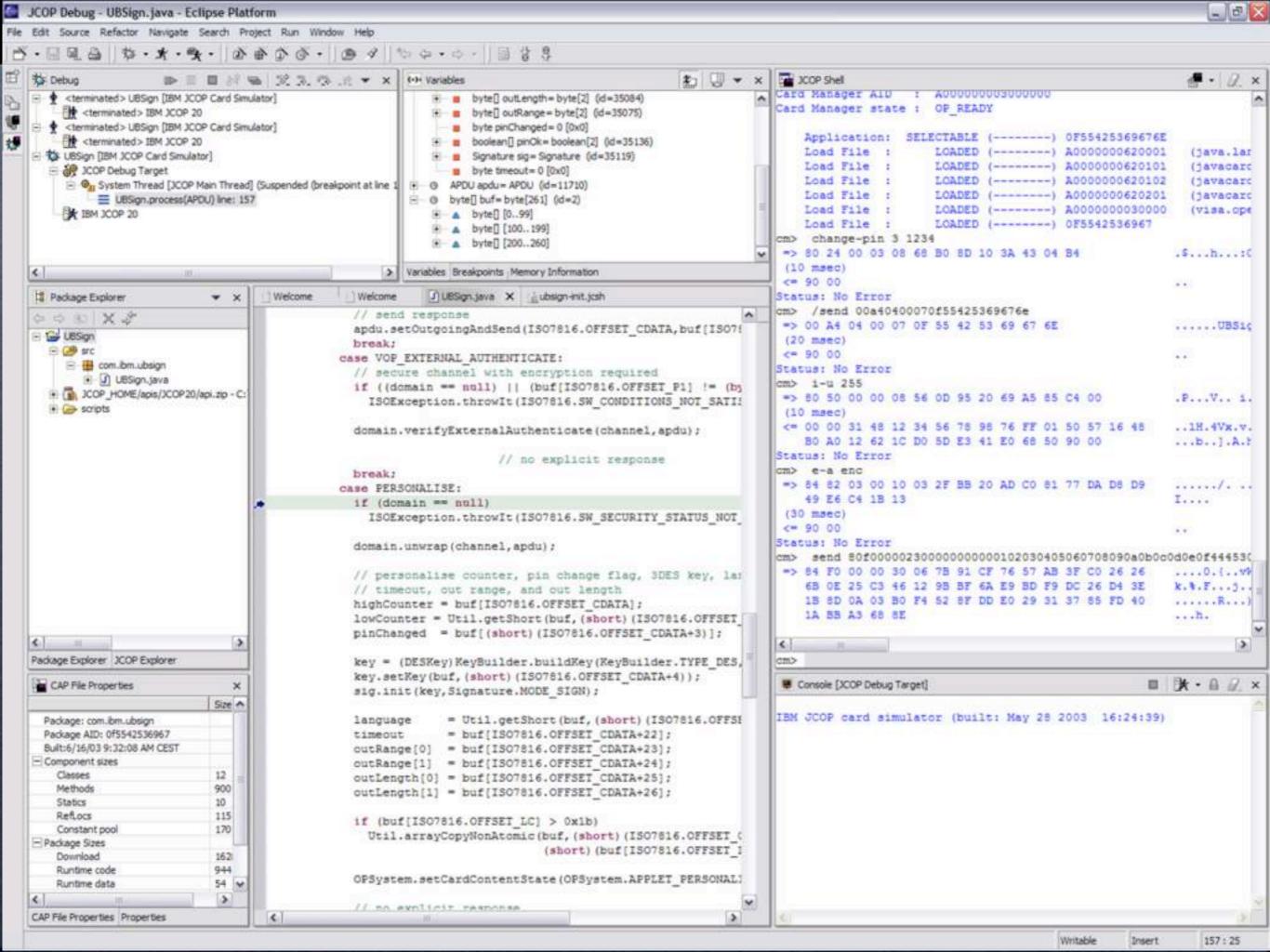


JavaCard Development

very close to Java development







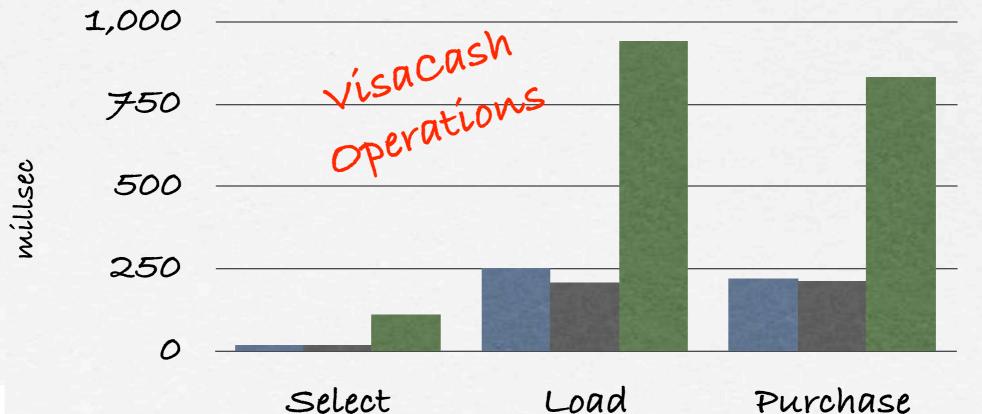


III JCOP

JCOP History

First release in 1997
Javacard is no longer slow







JCOP History





JCOP Variants Today

- JCOP 10 (DES)
- JCOP20 (RSA) [id]
- JCOP 30 (dual interface) I bio I
- JCOPSÍM (SWIM)



JCOP Memory Usage

| | ROM | EEPROM | |
|------------|-----------|-----------|--|
| | available | available | |
| JCOP 10/16 | 16 KB | 15 KB | |
| JCOP 20/8 | 16 KB | 7 KB | |
| JCOP 20/16 | 24 KB | 15 KB | |
| JCOP 20/32 | 56 KB | 31 KB | |
| JCOP 30/16 | 20 KB | 14 KB | |



JCOP Crypto Performance

| | 3DES | 1024 Bit | 1024 Bit |
|---------|-------|----------|----------|
| | CBC | Private | Public |
| JCOP 10 | 20 ms | n/a | n/a |
| JCOP 20 | 20 ms | 290 ms | 80 ms |
| JCOP 30 | 11 ms | 230 ms | 45 ms |



JCOP RSA Key Generation

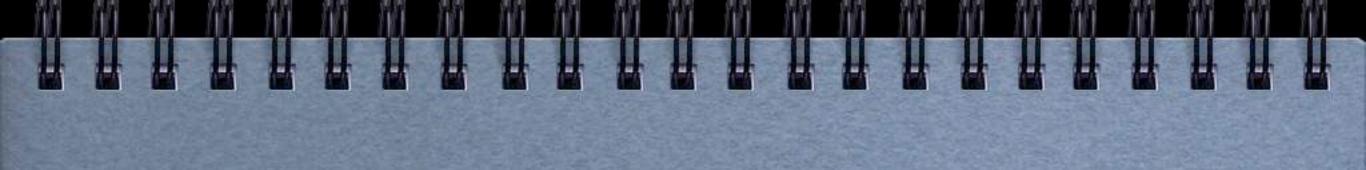
| | 512 Bít RSA CRT | 1024 Bít RSA CRT |
|---------|--------------------|---------------------|
| JCOP 20 | 2.2 secs | 6.9 secs |
| JCOP 30 | 2.1 secs | 6.1 secs |



Demo







Outlook



Next JavaCard

- Hardware
 - Moore's Law
 - More Interfaces (e.g., USB)
- Software
 - Remote Method Invocations (RMI)
 - Additional Crypto (e.g., ECC)



Questions?

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