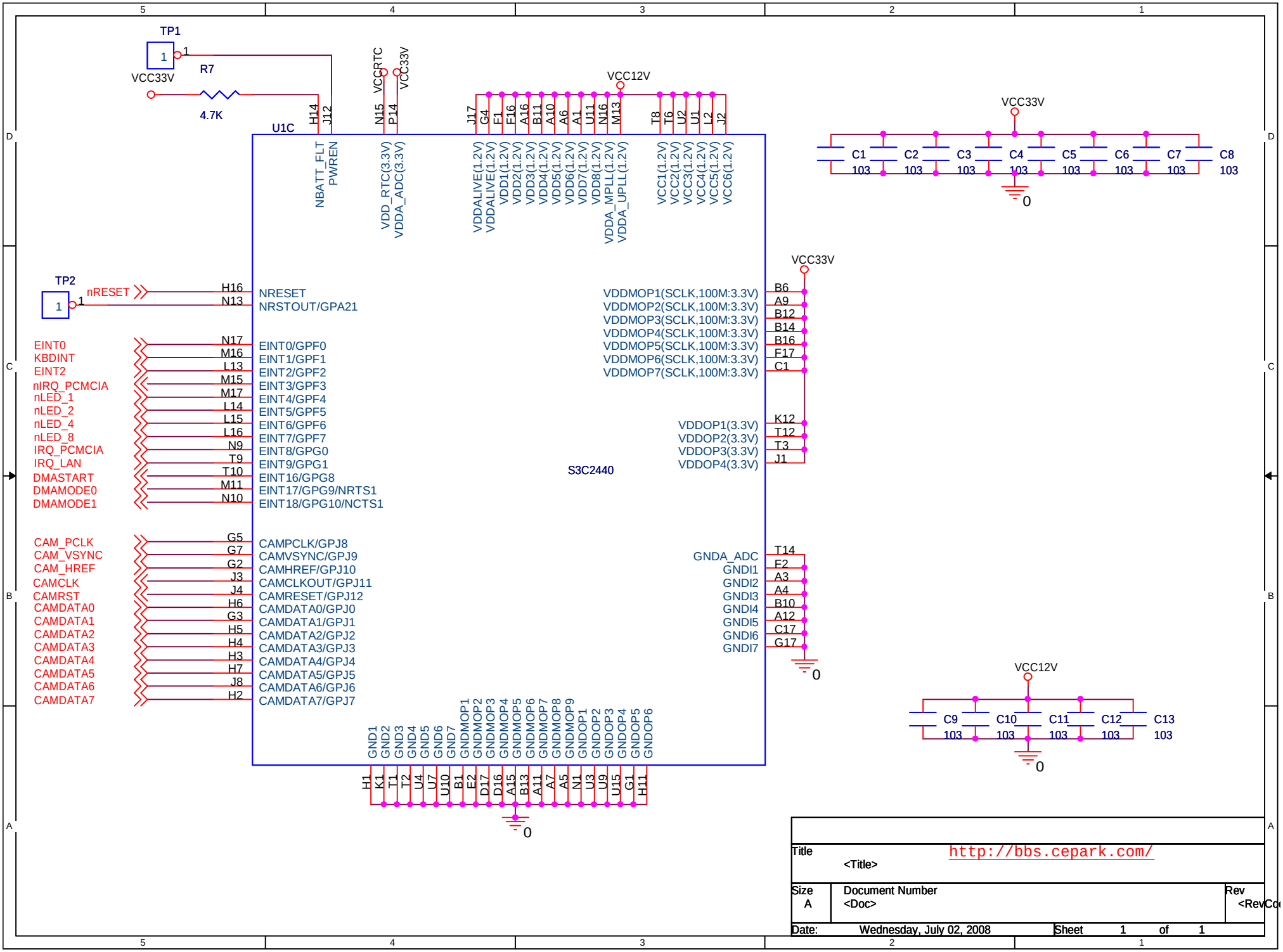
TP1
VCC33V
R7
4.7K
U1C
S3C2440
NBATT_FLT
PWREN
VDD_RTC(3.3V)
VDDA_ADC(3.3V)
VDDALIVE(1.2V)
VDD1(1.2V)
VDD2(1.2V)
VDD3(1.2V)
VDD4(1.2V)
VDD5(1.2V)
VDD6(1.2V)
VDD7(1.2V)
VDD8(1.2V)
VDDA_MPLL(1.2V)
VDDA_UPLL(1.2V)
VCC1(1.2V)
VCC2(1.2V)
VCC3(1.2V)
VCC4(1.2V)
VCC5(1.2V)
VCC6(1.2V)
VCC12V
VCCRTC
VCC33V
TP2
nRESET
NRESET
NRSTOUT/GPA21
EINT0
KBDINT
EINT2
nIRQ_PCMCIA
nLED_1
nLED_2
nLED_4
nLED_8
IRQ_PCMCIA
IRQ_LAN
DMASTART
DMAMODE0
DMAMODE1
EINT0/GPF0
EINT1/GPF1
EINT2/GPF2
EINT3/GPF3
EINT4/GPF4
EINT5/GPF5
EINT6/GPF6
EINT7/GPF7
EINT8/GPG0
EINT9/GPG1
EINT16/GPG8
EINT17/GPG9/NRTS1
EINT18/GPG10/NCTS1
CAM_PCLK
CAM_VSYNC
CAM_HREF
CAMCLK
CAMRST
CAMDATA0
CAMDATA1
CAMDATA2
CAMDATA3
CAMDATA4
CAMDATA5
CAMDATA6
CAMDATA7
CAMPCLK/GPJ8
CAMVSYNC/GPJ9
CAMHREF/GPJ10
CAMCLKOUT/GPJ11
CAMRESET/GPJ12
CAMDATA0/GPJ0
CAMDATA1/GPJ1
CAMDATA2/GPJ2
CAMDATA3/GPJ3
CAMDATA4/GPJ4
CAMDATA5/GPJ5
CAMDATA6/GPJ6
CAMDATA7/GPJ7
VDDMOP1(SCLK,100M:3.3V)
VDDMOP2(SCLK,100M:3.3V)
VDDMOP3(SCLK,100M:3.3V)
VDDMOP4(SCLK,100M:3.3V)
VDDMOP5(SCLK,100M:3.3V)
VDDMOP6(SCLK,100M:3.3V)
VDDMOP7(SCLK,100M:3.3V)
VDDOP1(3.3V)
VDDOP2(3.3V)
VDDOP3(3.3V)
VDDOP4(3.3V)
GNDA_ADC
GNDI1
GND2
GND3
GND4
GND5
GND6
GND7
GNDMOP1
GNDMOP2
GNDMOP3
GNDMOP4
GNDMOP5
GNDMOP6
GNDMOP7
GNDMOP8
GNDMOP9
GNDOP1
GNDOP2
GNDOP3
GNDOP4
GNDOP5
GNDOP6
C1 103
C2 103
C3 103
C4 103
C5 103
C6 103
C7 103
C8 103
C9 103
C10 103
C11 103
C12 103
C13 103
0
Title
<Title>
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Size
A
Document Number
<Doc>
Rev
<RevCo
Date: Wednesday, July 02, 2008
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